



ELECTRONICS, INC.
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NTE2930 MOSFET N-Channel, Enhancement Mode High Speed Switch

Features:

- Avalanche Rugged Technology
- Rugged Gate Oxide Technology
- Lower Input Capacitance
- Improved Gate Charge
- Extended Safe Operating Area
- Lower $R_{DS(on)}$: 0.032Ω Typ
- Lower Leakage Current: 10μA (Max) @ $V_{DS} = 100V$

Absolute Maximum Ratings:

Drain-to-Source Voltage, V_{DSS}	100V
Drain Current, I_D	
Continuous	
$T_C = +25^\circ C$	31A
$T_C = +100^\circ C$	21.9A
Pulsed (Note 1)	170A
Total Power Dissipation ($T_C = +25^\circ C$), P_D	100W
Derate Above $25^\circ C$	0.67W/ $^\circ C$
Gate-Source Voltage, V_{GS}	$\pm 20V$
Single Pulsed Avalanche Energy (Note 2), E_{AS}	641mJ
Avalanche Current (Note 1), I_{AR}	31A
Repetitive Avalanche Energy (Note 1), E_{AR}	10mJ
Peak Diode Recovery dv/dt (Note 3), dv/dt	6.5V/ns
Operating Junction Temperature Range, T_J	-55° to $+175^\circ C$
Storage Temperature Range, T_{stg}	-55° to $+175^\circ C$
Maximum Lead Temperature (During Soldering, 1/8" from case, 5sec), T_L	$+300^\circ C$
Thermal Resistance, Junction-to-Case, R_{thJC}	1.5 $^\circ C/W$
Thermal Resistance, Junction-to-Ambient, R_{thJA}	40 $^\circ C/W$

Note 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 2. $L = 1mH$, $I_{AS} = 31A$, $V_{DD} = 25V$, $R_G = 27\Omega$, Starting $T_J = +25^\circ C$.

Note 3. $I_{SD} \leq 40A$, $di/dt \leq 470A/\mu s$, $V_{DD} \leq V_{(BR)DSS}$, Starting $T_J = +25^\circ C$.

Electrical Characteristics: ($T_C = +25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain–Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	100	–	–	V
Breakdown Voltage Temperature Coefficient	$\Delta V_{(BR)DSS} / \Delta T_J$	$I_D = 250\mu A$	–	0.11	–	V/°C
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = 5V, I_D = 250\mu A$	2.0	–	4.0	V
Gate–Source Leakage Forward	I_{GSS}	$V_{GS} = 10V$	–	–	100	nA
Gate–Source Leakage Reverse	I_{GSS}	$V_{GS} = -10V$	–	–	-100	nA
Drain–to–Source Leakage Current	I_{DSS}	$V_{DS} = 100V$	–	–	10	μA
		$V_{DS} = 80V, T_C = +150^\circ\text{C}$	–	–	100	μA
Static Drain–Source ON Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 15.5A, \text{Note 4}$	–	–	0.04	Ω
Forward Transconductance	g_{fs}	$V_{DS} = 40V, I_D = 15.5A, \text{Note 4}$	–	27.3	–	mhos
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1\text{MHz}$	–	1750	2270	pF
Output Capacitance	C_{oss}		–	420	485	pF
Reverse Transfer Capacitance	C_{rss}		–	185	215	pF
Turn–On Delay Time	$t_{d(on)}$	$V_{DD} = 50V, I_D = 40A, R_G = 6.2\Omega, \text{Note 4, Note 5}$	–	17	50	ns
Rise Time	t_r		–	20	50	ns
Turn–Off Delay Time	$t_{d(off)}$		–	80	160	ns
Fall Time	t_f		–	45	100	ns
Total Gate Charge	Q_g	$V_{GS} = 10V, I_D = 40A, V_{DS} = 80V, \text{Note 4, Note 5}$	–	75	97	nC
Gate–Source Charge	Q_{gs}		–	13.2	–	nC
Gate–Drain (“Miller”) Charge	Q_{gd}		–	34.8	–	nC
Source–Drain Diode Ratings and Characteristics						
Continuous Source Current	I_S	(Body Diode)	–	–	31	A
Pulse Source Current	I_{SM}	(Body Diode) Note 1	–	–	170	A
Diode Forward Voltage	V_{SD}	$T_J = +25^\circ\text{C}, I_S = 31A, V_{GS} = 0V, \text{Note 4}$	–	–	1.6	V
Reverse Recovery Time	t_{rr}	$T_J = +25^\circ\text{C}, I_F = 40A, di_F/dt = 100A/\mu s, \text{Note 4}$	–	135	–	ns
Reverse Recovery Charge	Q_{rr}		–	0.65	–	μC

Note 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 4. Pulse Test: Pulse Width = 250 μs , Duty Cycle $\leq 2\%$.

Note 5. Essentially independent of operating temperature.

